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a. Explain features of IDE.

b. Explain files generated during the cross compilation.

c. Explain Javabased Embedded application development.

12EC118

(06 Marks)

(08 Marks)

(06 Marks)

M.Tech. Degree Examination, June / July 2014 **Advanced Embedded Systems**

Time: 3 hrs

111	ne: 3 hrs. Max. M	Aarks:100
· .	Note: Answer any FIVE full questions.	
1	 a. Explain elements of typical embedded systems. b. Differentiate between Harvard Architecture and von – Neumann architecture. c. Explain SRAM cell implementation. 	(08 Marks) (08 Marks) (04 Marks)
2	 a. Explain Operational Quality Attributes to be considered in embedded system des b. Explain different types of program memory. c. Explain with diagram I2C Bus and SPI bus interfacing. 	sign. (06 Marks) (06 Marks) (08 Marks)
3	a. Explain fundamental issues in Hardware software co design.b. Explain any three computational models in embedded system design.	(10 Marks) (10 Marks)
4	a. Explain super loop based approach.b. Explain with block diagram, assembly language to machine language conversion	
	c. Explain fundamental building blocks of UML.	(08 Marks) (04 Marks)
5	a. Explain with diagram OS Architecture and its services.b. Explain Monolithic Kernel.c. Explain multiprocessing and multitasking.	(08 Marks) (06 Marks) (06 Marks)
6	 a. Explain basic functions of Real time Kernel. b. What is IPC? Give an overview of different IPC mechanisms adopted by Kernel? c. Four processes with process IDs P1, P2, P3, P4 with estimated completion time milliseconds respectively enters the ready queue together in the order P1, I Calculate the waiting time, turn around time for each process, average waiting average turn around time for Round Robin scheduling Algorithm with time millisecond. 	(06 Marks) e 3, 6, 4, 2 P2, P3, P4. g time and e slice = 2
7	a. Define Device Drivers and explain its features.b. Explain functional requirements, while choosing an RTOs for embedded system of	(06 Marks) (08 Marks) lesign
	c. Explain deadlock.	(06 Marks) (06 Marks)
